APM32F030x8

Errata Sheet

Version: V1.0

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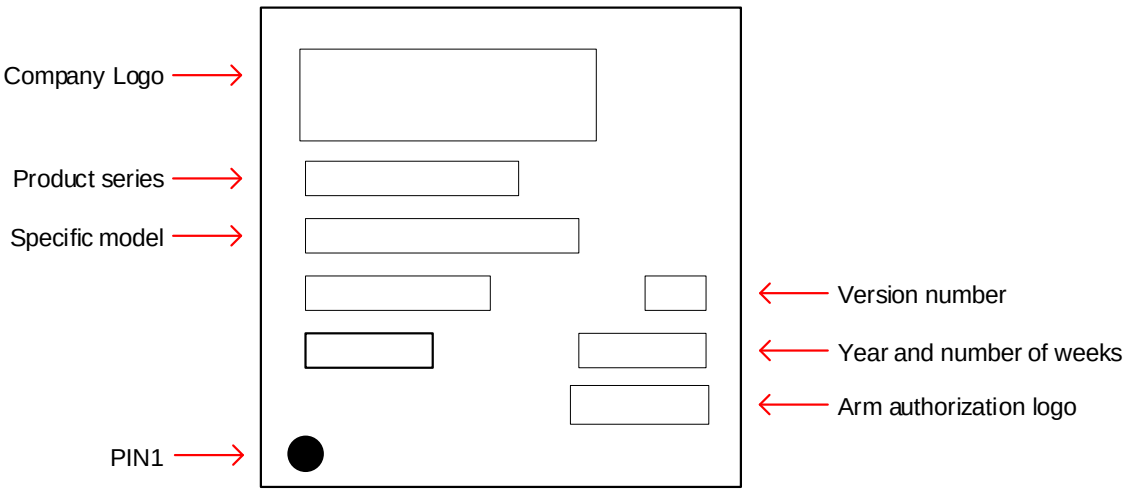
1 Introduction

This Manual mainly introduces the limitations of the APM32F030x8 series products during use. If you encounter the application scenarios described in the manual during the use of the product, please use the product according to the solutions provided in the manual; if no solution is provided, please avoid this application scenario.

2 Product Version and Silk Screen Printing Instructions

Figure 1 Product Version and Silk Screen Printing Instructions



3 Errata List

Table 1 Errata List

Category	Introduction	Product version
		B
Clock	HSE serves as the clock source	•
GPIO	BOOT pin	•
	Pin level delay	•
Tool	Burning	•

Note: "●" indicates that this errata description is involved in this version; the 'X' indicates that it is not involved in this version.

4 Clock

4.1 HSE serves as the clock source

Problem description

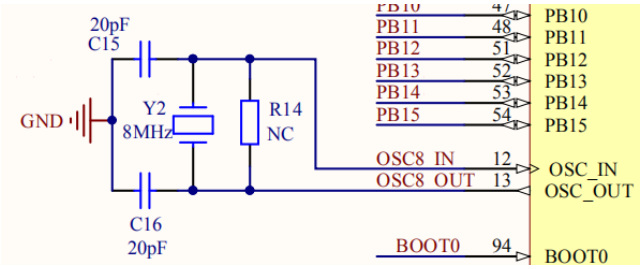
When the timeout value of the software that sets the HSE startup time is too small (e.g. 0x0500), external clock startup ready timeout may occur, which may result in the failure of using HSE as the clock source.

Solutions

To ensure normal startup of the crystal oscillator, it is recommended to modify the external clock wait time timeout value to at least 0x5000.

The macro definition of HSE_STARTUP_TIMEOUT can be modified. The macro definition is in XXX32F0xx.h. The recommended crystal oscillator circuit is shown below (the capacitance value should match the crystal oscillator model)：

Figure 2 Crystal Oscillator Circuit



Source program:

```
/*Enable HSE*/
RCC->CR |= ((uint32_t)RCC_CR_HSEON);
/*Wait till HSE is ready and if Timeout is reached exit*/
do
{
   HSEStatus = RCC->CR & RCC_CR_HSERDY;
   StartUpCounter++;
} while((HSEStatus == 0) && (StartUpCounter != HSE_STARTUP_TIMEOUT));
)
```

5 GPIO

5.1 BOOT pin

Problem description

Boot0 pin is suspending, and fails to start the program from Flash.

Solutions

It is recommended not to suspend the pins, but to set the pins to high or low level.

5.2 Pin level delay

Problem description

When Switching the I/O pin mode directly from "push-pull output high level" to "input mode", there is a level delay phenomenon. For example, at 55℃ and 3.3.V, there is a level delay phenomenon when switching from push-pull input high level to pull-down input.

Solutions

Choose either of the following solutions:

- After the push-pull output high-level is completed, insert the push-pull low-level output or configure as open-drain pull-up output mode, and then switch to input mode.
- Increase the duration of the input/output pull-down input mode (e.g. 3s).

6 Tool

6.1 Burning

Problem description

When the xxT packet is used on Keil 5.27 or 5.28 version, it cannot be burnt through AP-LINK, ULINK2 and ST-LINK.

Solutions

Choose either of the following solutions:

- Use APEXMIC.APM32F0xx_DPF or keil.xTM32F1xx.DFP.2.2.0.pack.
- Modify keil.xTM32F0xx.DFP.pdsc, and the specific operation is as follows:

(1) Look for keil.xTM32F1xx.DFP.pdsc under the installation directory of keil;

(2) Select the file, and right-click to choose the attributes;

(3) Remove the read-only attribute of the file;

(4) Open keil.xTM32F0xx.DFP.2.2.0.pack, and look for the location of Not a genuine xT Device! Abort connection;

(5) Try to find the following content:

```
<!--
Query(0,”Not a genuine xT Device! Abort connection”,1);
Message(2,”Not a genuine xT Device! Abort connection.”);
-->
```

(6) Log out.

7 Revision History

Table2 Document Revision History

Date	Version	Revision History
August 2024	1.0	New edition

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